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# भारतीय प्रौद्योगिकी संस्थान इंदौर

खंडवा रोड इंदौर 453 552

## **Indian Institute of Technology Indore**

Khandwa Road, Simrol Indore 453 552

### Advertisement for the post of JRF in Smart Radio Environment Research Project

Applications are invited from Indian nationals for the position of **Junior Research Fellow (JRF)** in an consotrium project **towards 6G** with IIT Delhi, IIT Indore, IIT Bhilai, IIT BHU, IIT Dhanbad, IIT Jodhpur, IIIT Bangalore, IIIT Naya Raipur and IIITDM Jabalpur.

**Title of the Project**: Smart Radio Environments, Implementation and deployment for targeted uses cases.

#### Principal Investigator (PI):

Prof. Vimal Bhatia,
Professor
Department of Electrical Engineering,
IIT Indore, Khandwa Road, Simrol,
Indore, Madhya Pradesh- 453552
(vbhatia@iiti.ac.in).
For other details and the project team

For other details and the project team, please visit the homepage-

https://sites.google.com/view/signalsoftware/sasg

Name of the position: Junior Research Fellow (JRF)/SRF/RA

Number of Positions: 01

#### **Essential qualifications for JRF:**

- 1. Graduate in Electrical Engineering/Electronics and Communication/Instrumentation/Mathematics/Computer Science/Computer Engneering, or M.Tech/ME/MS in Electrical Engineering/Electronics and Communication/Computer Science and Engineering or related areas.
- 2. Desirable qualified in any of the following National eligibility test/exams:
  - a. GATE
  - b. CSIR-UGC NET including lectureship (Assistant Professorship)
  - c. National-level examination conducted by Central Govt. Departments and their agencies and Institutions such as DST, DBT, DAE, DOS, DRDO, MHRD, ICAR, ICMR, IIT, IISc, IISER, etc

The candidate is encouraged to join the PhD program at IIT Indore

**Age limit for JRF:** 35 years

Salary JRF(per month): Rs. 37,000 + 16% HRA



### भारतीय प्रौद्योगिकी संस्थान इंदौर खंडवा रोड

खडवा राड इंदौर 453 552

# **Indian Institute of Technology Indore**

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Khandwa Road, Simrol Indore 453 552

**Desirable**: Experience in any of the following is desired. Embedded systems, Wireless Communications, Analog and digital board design, fabrication, antenna design, testing and integration, knowledge in signal processing, communication systems and wireless systems, Programming experience in C, C++, Python, MATLAB, and Network programming (using REST APIs).

**Duration:** The initial appointment will be given for six months and is extendable based on performance till the project completion (Total project duration of more than 3 years).

**How to apply**: Interested candidates must email their detailed CVs to the PI so as to reach him by 08/01/2024. Only shortlisted candidates will be called for the interview. The positions are available immediately. Clearly mention in the email title the position being applied for with subject line "**Applicant Smart Radio Environment: JRF**".

Due date: The application must reach the PI, (vbhatia@iiti.ac.in), by 08/01/2024.

#### **Terms & Conditions:**

- i. No TA/DA will be provided to the candidate for the interview.
- ii. The PI shall not be responsible for email delay if any, or any other reason for non-receipt of the document at the specified time and will result in disqualification/rejection of the application.
- iii. The decision of the selection committee will be final.
- iv. If the number of shortlisted candidates called for the interview is large, the selection committee may decide to restrict the number of candidates for the interview to a reasonable limit after considering qualifications and experience over and above the minimum prescribed in the advertisement.
- v. The appointment of the candidate will be governed by the terms and conditions of the Institute/ funding agency particularly applicable to the said project as and when required.
- vi. The selected candidate will have to join duty immediately on receipt of the offer.
- vii. The fellowship may be terminated with a 30-day notice before completion of the tenure if performance till date is not deemed satisfactory.



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